

**Notice of References Cited**

Application/Control No.

09/436,522

Applicant(s)/Patent Under  
Reexamination  
SHA ET AL.

Examiner

Edith M Chang

Art Unit

2634

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,590,949	07-2003	Marten et al.	375/376
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Young et al., "A PLL clock generator with 5 to 10 MHz of lock range for microprocessors", Solid-State Circuits, IEEE Journal of, Volume:27, Issue:11, Nov. 1992 Pages:1599-1607
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.